

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

1300 I STREET, N. W.
WASHINGTON, DC 20005-3315

202 • 408 • 4000
FACSIMILE 202 • 408 • 4400

WRITER'S DIRECT DIAL NUMBER:

ATLANTA
404 • 653 • 6400
PALO ALTO
650 • 849 • 6600

TOKYO
011 • 813 • 3431 • 6943
BRUSSELS
011 • 322 • 646 • 0353

(202) 408-4024

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Box Patent Application
Assistant Commissioner for Patents
Washington, D.C. 20231

New U.S. Patent Application

Title: METHOD, APPARATUS, AND COMPUTER PROGRAM OF
SEARCHING FOR CLUSTERING FAULTS IN SEMICONDUCTOR DEVICE
MANUFACTURING

Inventors and Addresses:

Kunihiro MITSUTAKE
Tokyo, Japan

Yukihiro USHIKU
Tokyo, Japan

Sir:

We enclose the following papers for filing in the United States Patent and
Trademark Office in connection with the above patent application.

1. Application - 47 pages, including 11 independent claims and 22 claims total.
2. Drawings - 24 sheets of formal drawings containing 33 figures.
3. Information Disclosure Statement and Information Disclosure Citation, PTO 1449 with document attached.

Je971 U.S. PTO
09/931916
08/20/01

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4. The filing fee is calculated as follows:

					\$710	\$ 710.00
Basic Application Filing Fee	Number of Claims		Basic	Extra Claims		
Total Claims	22	-	20	2	x \$18	36.00
Independent Claims	11	-	3	8	x \$80	640.00
[] Presentation of Multiple Dep. Claim(s)					+\$270	
					Subtotal	\$ 1386.00
					Reduction by 1/2 if small entity	-
					TOTAL APPLICATION FILING FEE	\$ 1386.00

5. A check for \$1386 is enclosed. The fee includes:

\$710 filing fee;
\$676 additional claims fee; and

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicants claim the right to priority based on Japanese Patent Application No. 2000-249718, filed August 21, 2000 and Japanese Patent Application No. 2001-65338, filed March 8, 2001.

Please address all correspondence with respect to this application to:

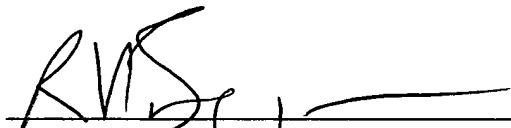
Finnegan, Henderson, Farabow,
Garrett & Dunner, L.L.P.
1300 I Street, N.W.
Washington, D.C. 20005-3315

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Assistant Commissioner of Patents
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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or § 1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By: 
Richard V. Burgujian
Reg. No. 31,744

RVB/FPD/dvz
Enclosures

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